



# FPL 2011

## 21st International Conference on Field Programmable Logic and Applications (FPL) Chania, Crete, GREECE, Sep. 5-7, 2011 <http://www.fpl.org>

### Call For Papers

The International Conference on Field Programmable Logic and Applications (FPL) is the first and largest conference covering the rapidly growing area of field-programmable logic. During the past 20 years, many of the advances achieved in reconfigurable architectures, applications, design methods and tools have been first published in the proceedings of the FPL conference series. Its objective is to bring together researchers and industry from all over the world for a wide ranging discussion on FPGAs, including, but not limited to: applications, advanced electronic design automation (EDA), novel system architectures, embedded processors, arithmetic, and dynamic reconfiguration.

FPL is organized yearly in Europe and attended by top-level scientists and researchers. The 21st FPL continues the tradition of the previous editions and will be hosted by the Technical University of Crete at Chania, Greece, in September 5-7, 2011.

The Program Committee cordially invites you to participate and submit your contribution to FPL 2011. The conference topics include, but are not limited to:

#### Reconfigurable Architectures

- Dynamic, partial, run-time reconfiguration
- Low power architectures
- Defect and fault tolerance
- Self-aware systems
- Self-configuration, self-testing, self-healing
- Adaptive communication infrastructure
- Reconfigurable embedded systems
- Interconnects and NoC's
- Field programmable analog arrays
- FPGA's vs. Multicores/GPU's

#### Applications

- Bioinformatics
- Communications and networking
- Application acceleration
- Medical solutions
- Experiments for high energy physics
- Astronomy
- Aerospace
- Biologically inspired systems
- Computational fluid dynamics (CFD)

#### Design Methods and Tools

- Dynamic, partial, run-time reconfiguration
- CAD for reconfigurable architectures
- Logic optimization and technology mapping
- Placement and routing algorithms
- System-level design tools
- Testing, verification and benchmarking
- Hardware/software co-design
- Compilers and languages
- Rapid prototyping
- Radiation tolerance and reliability

#### Surveys, Trends and Education

- Roadmap of reconfigurable computing
- Teaching reconfigurable systems
- Open source designs
- History/surveys of reconfigurable logic
- Emerging device technologies
- Tutorials

Authors are invited to submit original and unpublished contributions as either 6 page papers to be considered as regular papers (submissions accepted as posters will have 4 pages) or 2 page extended abstracts for PhD forum contributions. All contributions must be submitted electronically in PDF format via the web page linked at <http://www.fpl.org>.

Each accepted paper MUST have at least an author with a paid full registration for the manuscript to be included and published in the proceedings; an author is also expected to attend and present the paper at the Conference.

FPL's PhD forum is intended as a venue for PhD students to present their work and receive feedback from other researchers. Papers accepted to the PhD forum will be published in the conference proceedings and presented at a special poster session during the conference. Contributions to the PhD forum based on preliminary results of work in progress are particularly encouraged.

Proposals for tutorials within the technical scope of the conference are solicited. Submissions should be done through email directly to the Program co-Chairs ([pnevmati@ece.tuc.gr](mailto:pnevmati@ece.tuc.gr), [athanas@vt.edu](mailto:athanas@vt.edu)).

<b>Important dates:</b>	<b>Extended Submission Deadline – (FINAL):</b>	■ <b>March 28, 2011 (23:59 GMT)</b>
	Acceptance notification:	■ <b>May 20, 2011</b>
	Camera-ready deadline:	■ <b>June 14, 2011</b>
	Author registration deadline:	■ <b>June 14, 2011</b>

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